

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: T. MIYAMOTO et al  
Serial No. 10/046,446  
Filed: January 16, 2002  
Group Art Unit: 2811  
Examiner: D. Owens

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THAT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Alexandria, VA 22313

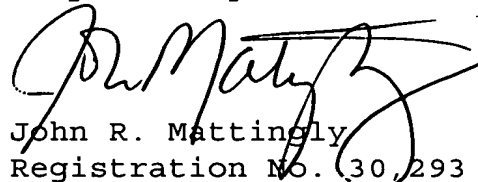
Sir:

In accordance with the duty of disclosure, the applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/046,446.

The above-referenced application is a divisional application of U.S. Application Serial No. 10/046,446, filed January 16, 2002.

The Applicant requests the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,



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Date: February 5, 2004

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. T&A-110-02		SERIAL NO.		
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT T. MIYAMOTO et al				
				FILING DATE February 5, 2004		GROUP		
<b>U.S. PATENT DOCUMENTS</b>								
* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)	
	AA	2002/ 0076913	06/2002	Lee, Joo-hern				
	AB	2002/ 0125568	09/2002	Jiang et al				
	AC	6,469,393	10/2002	Oya, Y.				
	AD	2003/ 0153099	08/2003	Jiang et al				
	AE	6,103,552	08/2000	Lin, Mou-Shiung				
	AF	6,621,164	09/2003	Hwang et al				
	AG							
	AH							
	AI							
	AJ							
	AK							
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL	WO99/ 23696	5/14/99	WIPO			<input type="checkbox"/>	<input type="checkbox"/>
	AM	7-161761	6/23/95	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AN	11-40563	2/12/99	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AO						<input type="checkbox"/>	<input type="checkbox"/>
	AP						<input type="checkbox"/>	<input type="checkbox"/>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>								
	AR		"Electronics Mount Technology Extra Edition 2000", Gijyutsu-chyuosa-kai Corp., issued May 28, 2000, pp. 81-113.					
	AS							
	AT							